<u>TOSHIBA</u>

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHCT14AF,TC74VHCT14AFN,TC74VHCT14AFT,TC74VHCT14AFK

Hex Schmitt Inverter

The TC74VHCT14A is an advanced high speed CMOS SCHMITT INVERTER fabricated with silicon gate C²MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

Pin configuration and function are the same as the TC74VHC04 but the inputs have hysteresis and with its schmitt trigger function, the TC74VHC14 can be used as a line receivers which will receive slow input signals.

The input voltage are compatible with TTL output voltage.

This device may be used as a level converter for interfacing 3.3 to 5 V system.

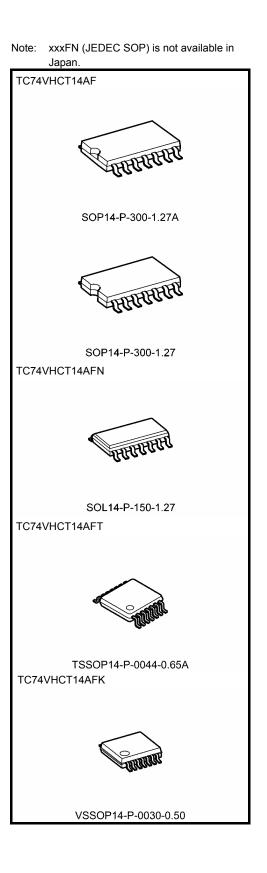
Input protection and output circuit ensure that 0 to 5.5 V can be applied to the input and output ^(Note) pins without regard to the supply voltage. These structure prevents device destruction due to mismatched supply and input/output voltages such as battery back up, hot board insertion, etc.

Note: $V_{CC} = 0 V$

Features

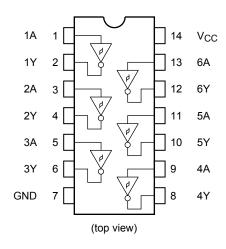
- High speed: $t_{pd} = 5.0 \text{ ns}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 2 \mu A \text{ (max)}$ at $Ta = 25^{\circ}C$
- Compatible with TTL inputs
 - : V_{IL} = 0.8 V (max) V_{IH} = 2.0 V (min)
- Power down protection is provided on all inputs and outputs.
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Low noise: $V_{OLP} = 0.8 V (max)$
- Pin and function compatible with the 74 series (74AC/HC/F/ALS/LS etc.) 14 type.

Weight	
SOP14-P-300-1.27A	: 0.18 g (typ.)
SOP14-P-300-1.27	: 0.18 g (typ.)
SOL14-P-150-1.27	: 0.12 g (typ.)
TSSOP14-P-0044-0.65A	: 0.06 g (typ.)
VSSOP14-P-0030-0.50	: 0.02 g (typ.)



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Pin Assignment



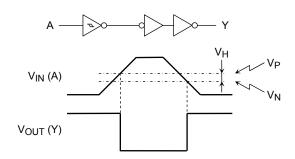
IEC Logic Symbol

1A	Д	(2) 1Y
2A_(3)		(4) 2Y
3A_(5)		(6) 3Y
4A_(9)		(8) 4Y
5A_(11)		(10) 5Y
6A_(13)		(12) 6Y

Truth Table

А	Y
L	Н
Н	L

System Diagram, Waveform



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit				
Supply voltage range	V _{CC}	-0.5 to 7.0	V				
DC input voltage	V _{IN}	-0.5 to 7.0	V				
DC output voltage	Varia	-0.5 to 7.0 (Note 2)					
	Vout	-0.5 to V _{CC} + 0.5 (Note 3)	V				
Input diode current	lık	-20	mA				
Output diode current	IOK	±20 (Note 4)	mA				
DC output current	IOUT	±25	mA				
DC V _{CC} /ground current	ICC	±50	mA				
Power dissipation	PD	180	mW				
Storage temperature	T _{stg}	-65 to 150	°C				

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note 2: $V_{CC} = 0 V$

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$

Recommended Operating Conditions (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	Varia	0 to 5.5 (Note 2)	V
	Vout	0 to V _{CC} (Note 3)	v
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 20	ns/V

Note 1: The recommended operating conditions are required to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Note 2: $V_{CC} = 0 V$

Note 3: High or low state

Electrical Characteristics

DC Characteristics

Characteristics	Characteristics Symbol Test Condition		Test Condition		-	Ta = 25°0	5°C		a = 0 85°C	Unit	
onaracteristics	Symbol				Min	Тур.	Max	Min	Max	Onic	
Positive threshold	M			4.5	-	_	1.90	_	1.90	V	
voltage	VP		—	5.5	—	—	2.10	-	2.10	v	
Negative threshold	V _N			4.5	0.50	_		0.50	—	V	
voltage	۷N		—	5.5	0.60	—		0.60	—	v	
	M			4.5	0.40	_	1.40	0.40	1.40	V	
Hysteresis voltage	V _H		—		0.40	—	1.50	0.40	1.50	v	
High-level output	Varia	VIN	I _{OH} = −50 μA	4.5	4.4	4.5	_	4.4	_	v	
voltage	Voh	= V _{IL} I _{OH} = -8 mA		4.5	3.94	_	_	3.80	_	v	
Low-level output	Mai	V _{IN}	I _{OL} = 50 μA	4.5	_	0.0	0.1	_	0.1	V	
voltage	V _{OL}	= V _{IH}	I _{OL} = 8 mA	4.5	_	_	0.36	_	0.44		
Input leakage current	I _{IN}	V _{IN} = 5.8	V _{IN} = 5.5 V or GND		_	_	±0.1	_	±1.0	μA	
	ICC	$V_{IN} = V_{C}$	CC or GND	5.5	-	-	2.0	-	20.0	μA	
Quiescent supply current	Ісст	Per input: V _{IN} = 3.4 V Other input: V _{CC} or GND		5.5	_	_	1.35	_	1.50	mA	
Output leakage current	I _{OPD}	V _{OUT} =	5.5 V	0	_	_	0.5		5.0	μA	

AC Characteristics (input: tr = tf = 3 ns)

Characteristics	Symbol		st Condition		Ta = 25°C			Ta = −40 to 85°C		Unit
	- ,		V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	
Propagation delay	t _{pLH}	— 5.0 ± 0.5	15	_	5.0	7.6	1.0	9.0	ns	
time	t _{pHL}		- 5.0 1 0.5	50	—	6.5	9.6	1.0	11.0	115
Input capacitance	C _{IN}		_		—	4	10		10	pF
Power dissipation capacitance	C _{PD}			(Note)	—	18			—	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation: $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6$ (per gate)

Noise Characteristics (input: tr = tf = 3 ns)

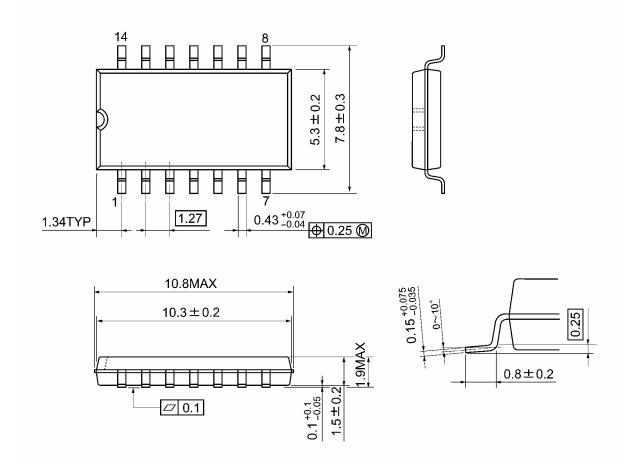
			Ta =			
Characteristics	Symbol		V _{CC} (V)	Тур.	Limit	Unit
Quiet output maximum dynamic V_{OL}	V _{OLP}	C _L = 50 pF	5.0	0.5	0.8	V
Quiet output minimum dynamic V_{OL}	V _{OLV}	C _L = 50 pF	5.0	-0.5	-0.8	V
Minimum high level dynamic input voltage	VIHD	C _L = 50 pF	5.0	—	2.0	V
Maximum low level dynamic input voltage	V _{ILD}	C _L = 50 pF	5.0	-	0.8	V



Package Dimensions

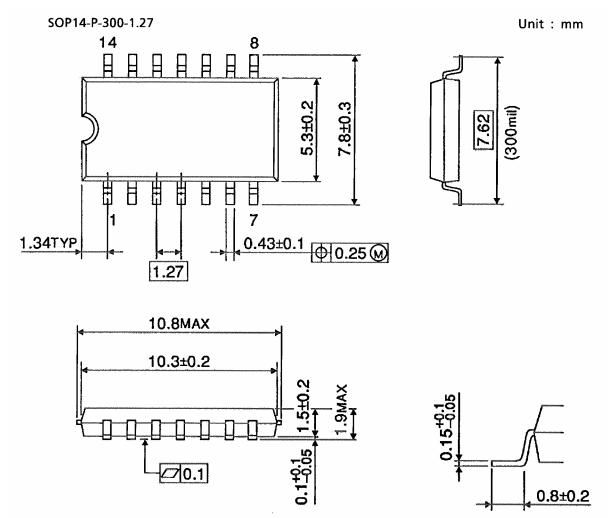
SOP14-P-300-1.27A

Unit: mm



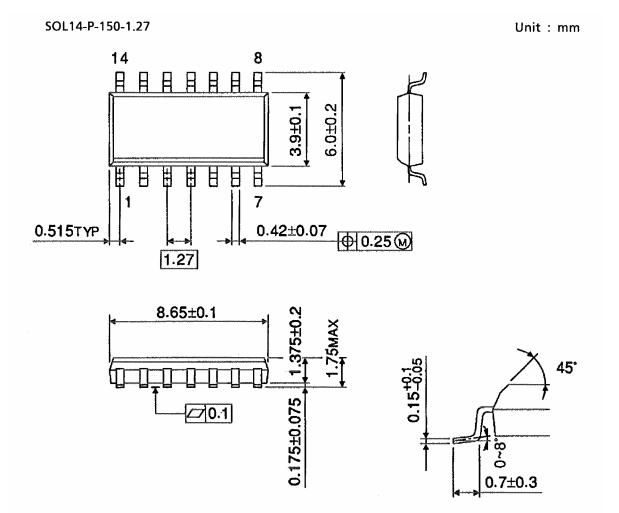
Weight: 0.18 g (typ.)

Package Dimensions



Weight: 0.18 g (typ.)

Package Dimensions (Note)



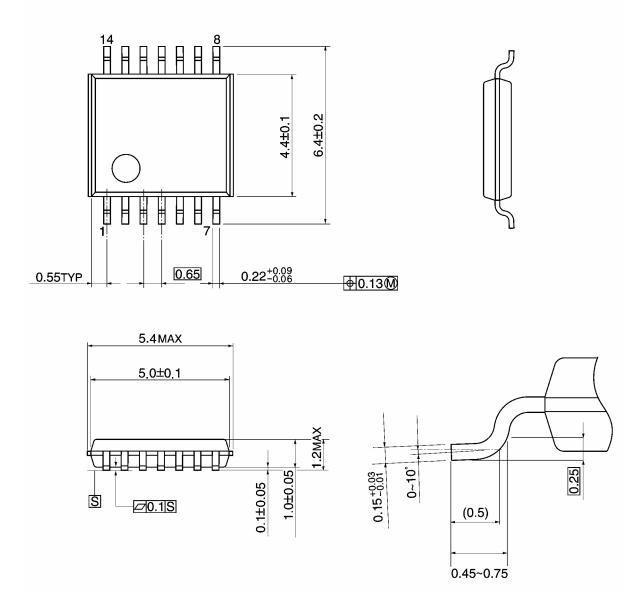
Note: This package is not available in Japan.

Weight: 0.12 g (typ.)

Package Dimensions

TSSOP14-P-0044-0.65A

Unit: mm



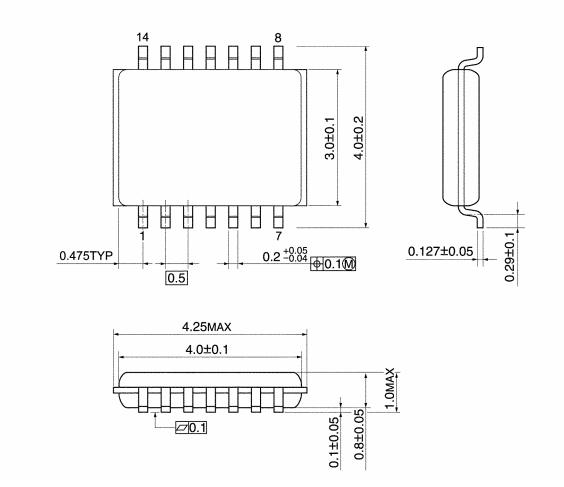
Weight: 0.06 g (typ.)

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Package Dimensions

VSSOP14-P-0030-0.50

Unit: mm



Weight: 0.02 g (typ.)

Note: Lead (Pb)-Free Packages

SOP14-P-300-1.27A SOL14-P-150-1.27 TSSOP14-P-0044-0.65A VSSOP14-P-0030-0.50

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